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10/075868



PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10075868	FILING DATE 02/13/2002	CLASS 370	SUBCLASS 724	GAU 2661	EXAMINER <i>Rocady</i>
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**\*\*APPLICANTS:** Schmid Josef; *2109*  
*2133* *TAB ONE*

**\*\*CONTINUING DATA VERIFIED:**

**\*\* FOREIGN APPLICATIONS VERIFIED:**  
EUROPEAN PATENT OFFICE (EPO) 01301472.5 02/19/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 3
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE : Boundary scan delay chain for cross-chip delay measurement		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-4361 (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner	
		Application Examiner	
<b>PREPARED FOR ISSUE</b>		<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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